

8 7 6 5 4 3 2 1

D

D

# CORONA TRANSLATION BOARD

C

C

PAGE	CONTENTS	REV	COMMENTS
[1]	COVER PAGE	0.1	STARTED BY DENNIS BASED ON FAB B BOARD
[2]	AVIP AND BORON INTERFACE		ADDED WINCHESTER FTFS AND RELATED CIRCUITS
[3]	CLOCK SIGNALS AND GROUND		ADDED EXT JTM TEST POINT
[4]	DCLF CONNECTIONS		ADDED JTAG BUFFER & ADDED BACK SPKR INTERFACE
[5]	UNUSED CONNECTORS	0.2	ADDED FT1000 - IR_DATA
[6]	JTAG INTERFACES	A	RELEASE TO LAYOUT
[7]	SMC AND SPI INTERFACES		
[8]	DEBUG AND THERMAL CAL INTERFACES		
[9]	USB AND WAVE INTERFACES		
[10]	HDMI AND MMC INTERFACES		
[11]	HDD AND ODD INTERFACES		
[12]	ETHERNET AND POWER SUPPLY		
[13]	BUFFER POWER SUPPLY		

B

B

A

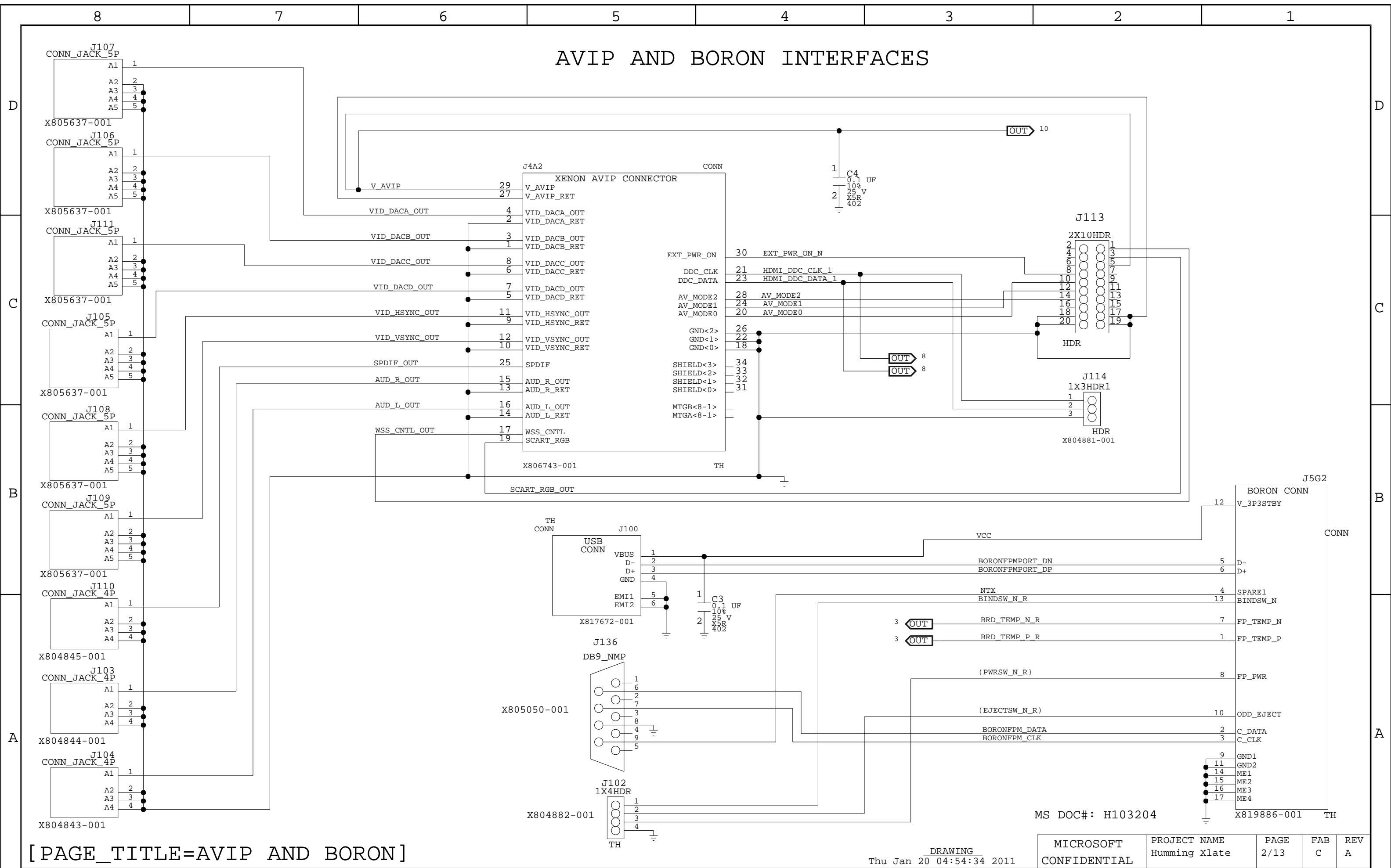
A

MS DOC#: H103204

MICROSOFT CONFIDENTIAL	PROJECT NAME Humming Xlate	PAGE 1/13	FAB C	REV A
---------------------------	-------------------------------	--------------	----------	----------

8 7 6 5 4 3 2 1

# AVIP AND BORON INTERFACES



[PAGE\_TITLE=AVIP AND BORON]

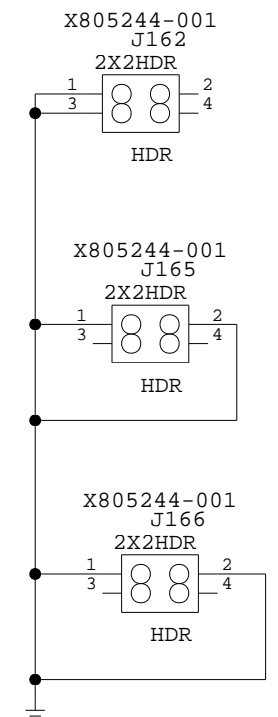
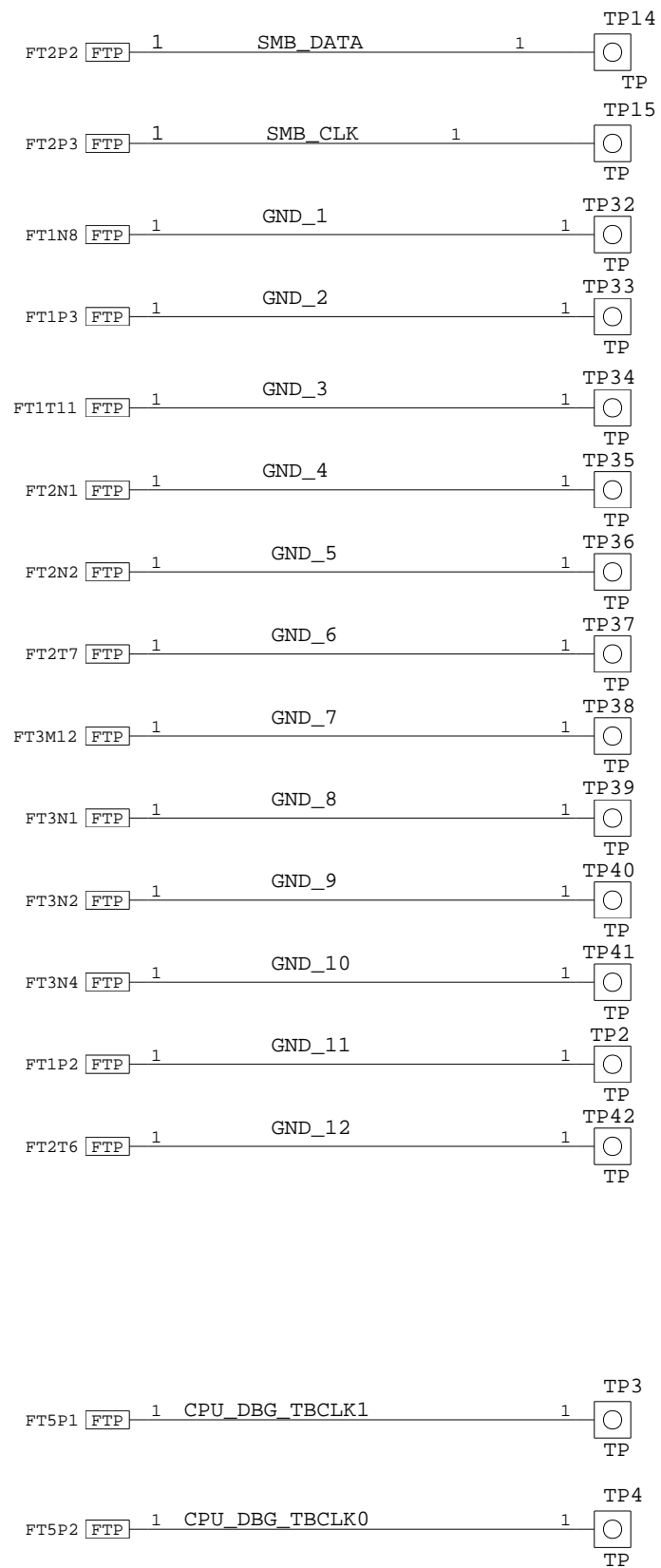
DRAWING  
Thu Jan 20 04:54:34 2011

MICROSOFT CONFIDENTIAL	PROJECT NAME Humming Xlate	PAGE 2/13	FAB C	REV A
---------------------------	-------------------------------	--------------	----------	----------

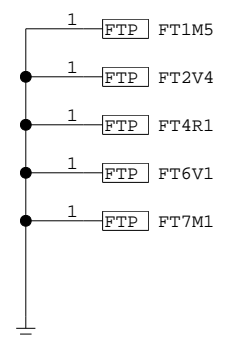
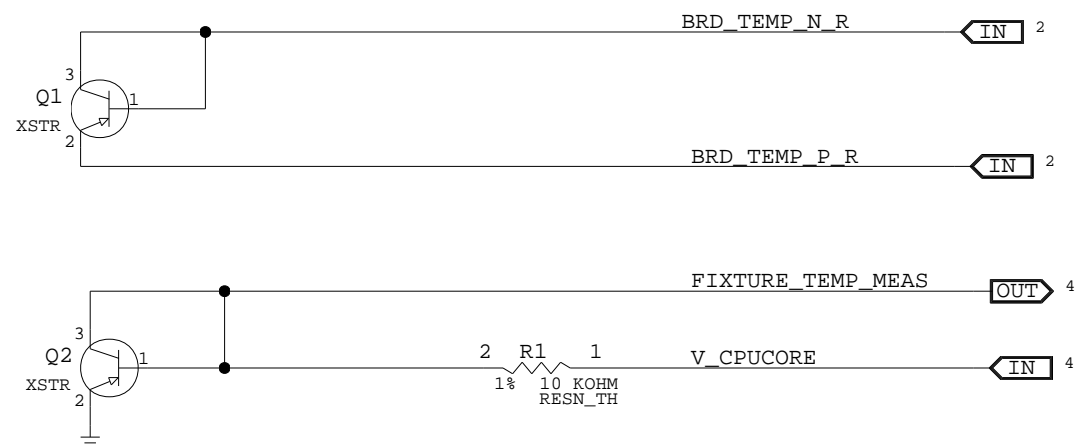
MS DOC#: H103204

X819886-001 TH

### CLOCK SIGNALS AND GROUND

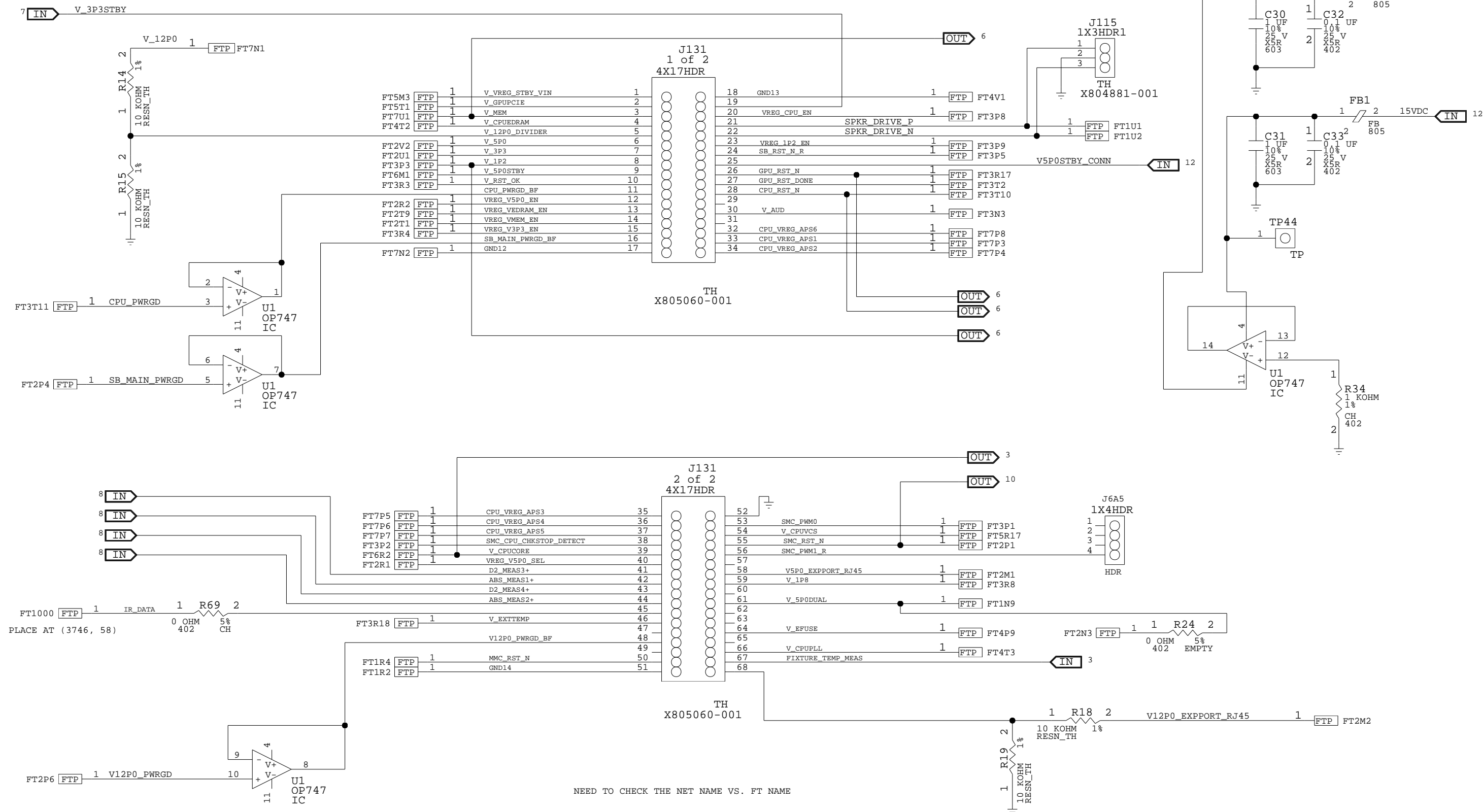


1 [FTP] FT3R2  
 FT3R7 [FTP] 1  
 STILL DRILL HOLES FOR THESE 2 FTPS



MS DOC#: H103204

### DCLF CONNECTIONS

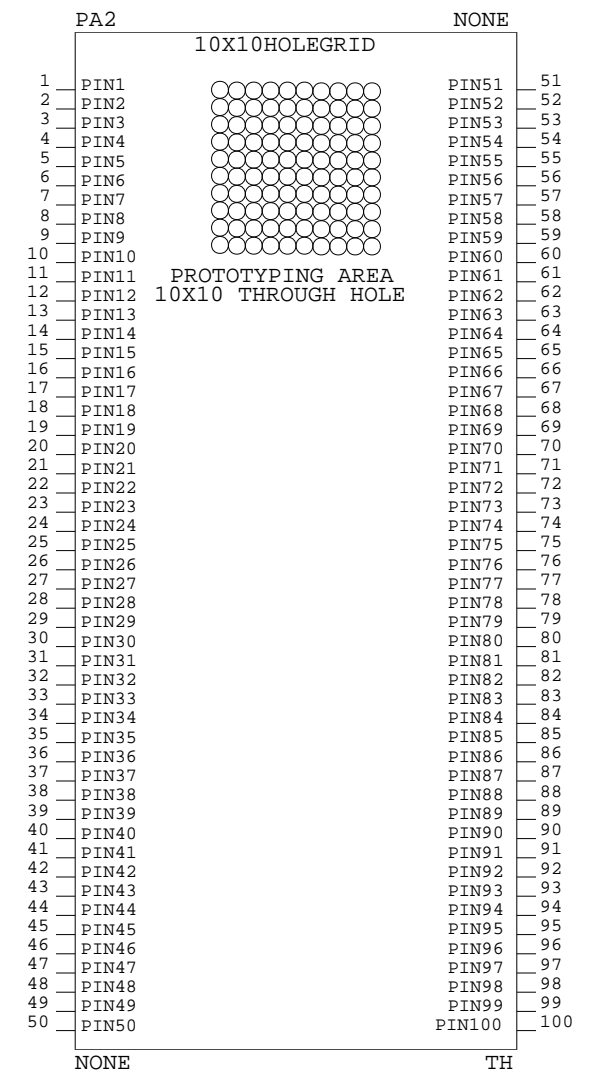
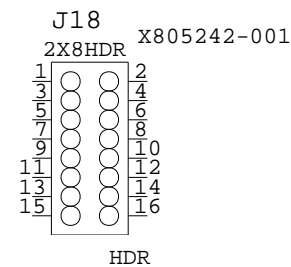
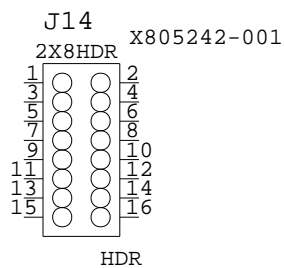
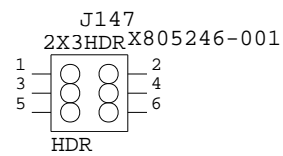
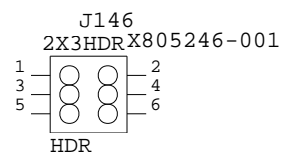
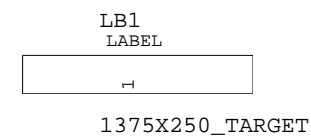
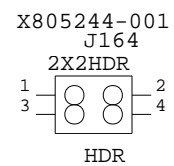
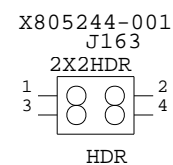
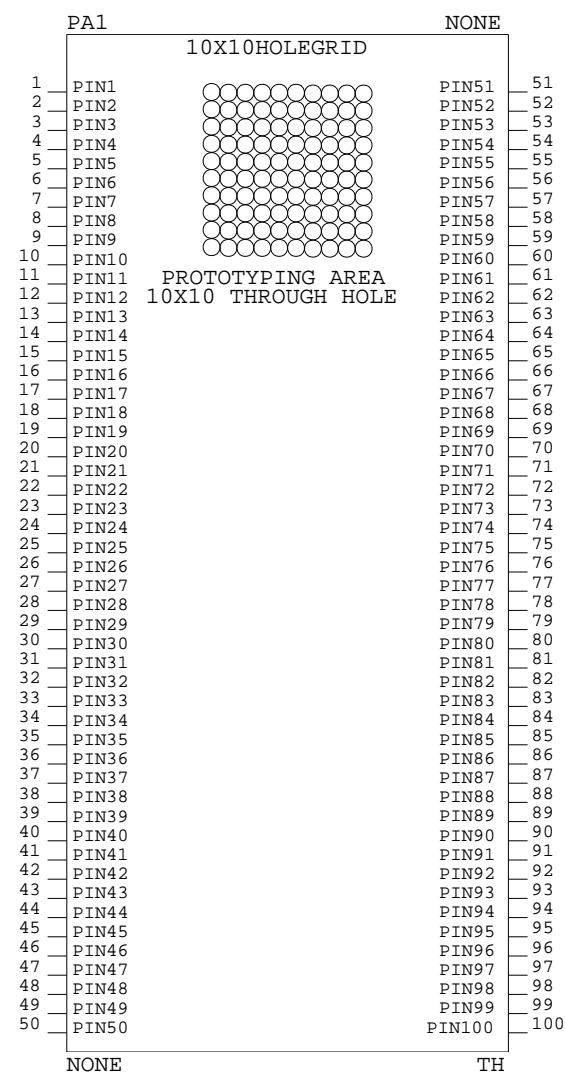


NEED TO CHECK THE NET NAME VS. FT NAME

MS DOC#: H103204

8 7 6 5 4 3 2 1

# UNUSED CONNECTORS



MS DOC#: H103204

8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

### JTAG INTERFACES

D

D

C

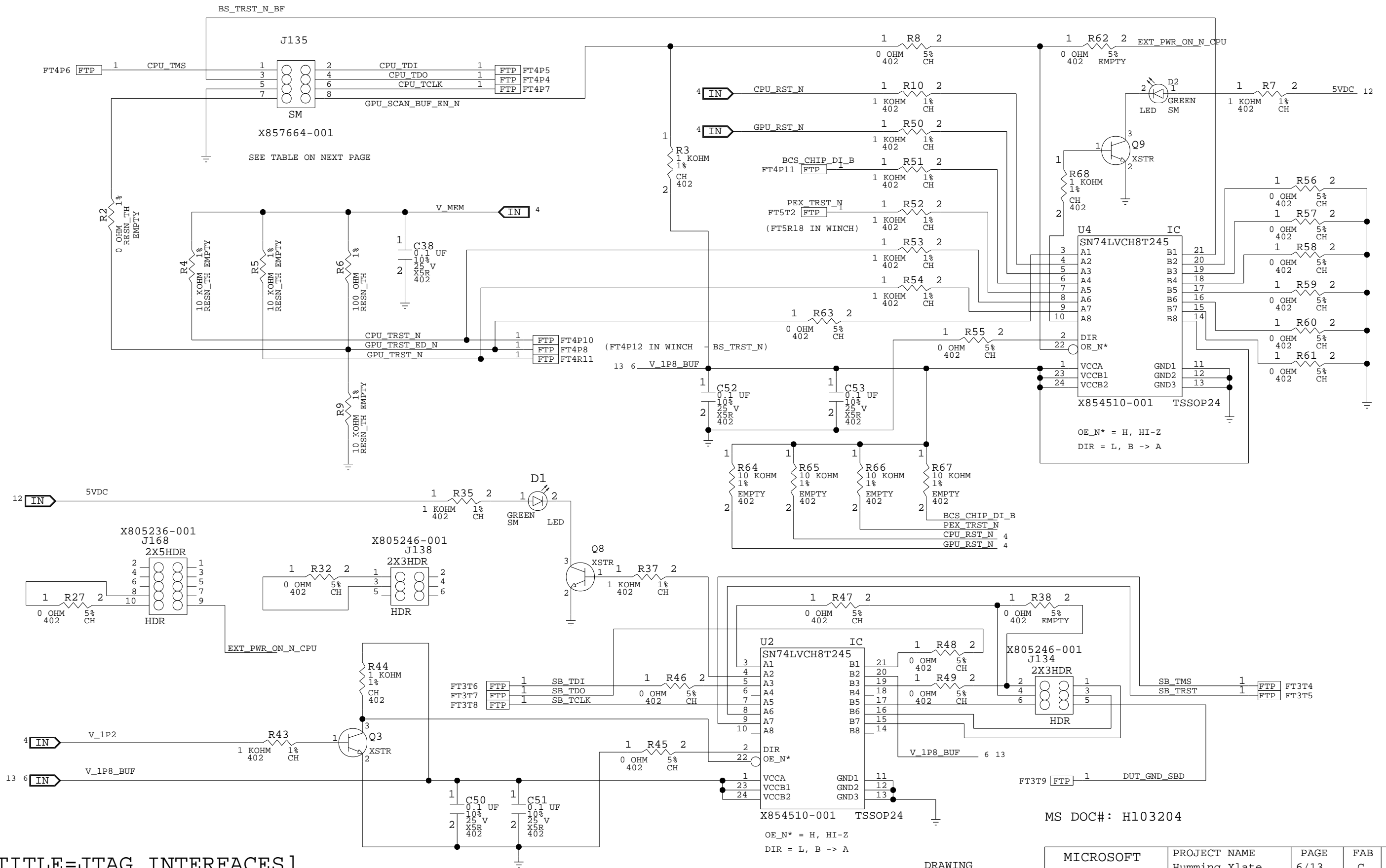
C

B

B

A

A



MS DOC#: H103204

[PAGE\_TITLE=JTAG INTERFACES]

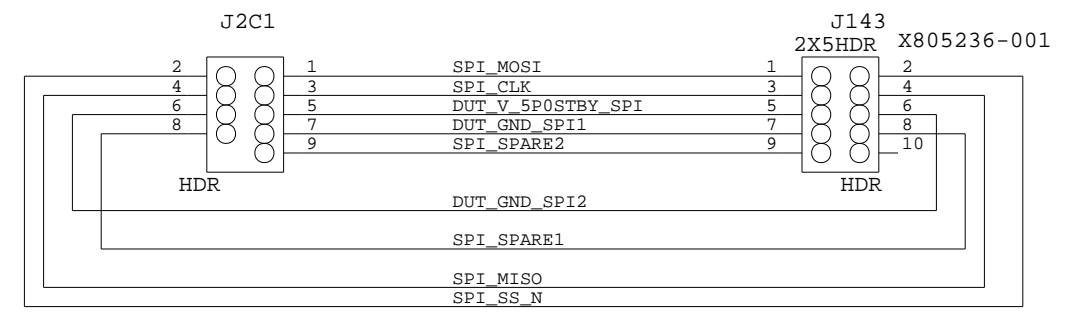
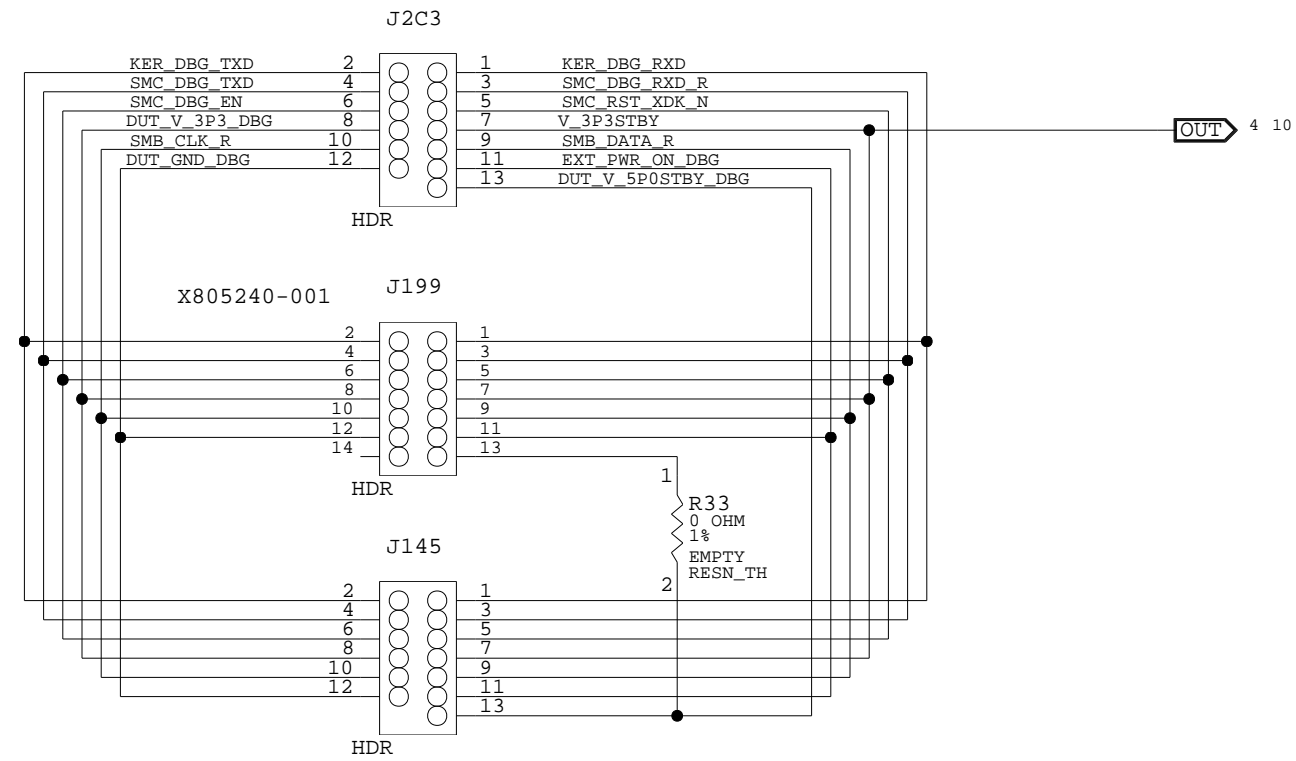
DRAWING  
Thu Jan 20 04:54:35 2011

MICROSOFT CONFIDENTIAL	PROJECT NAME Humming Xlate	PAGE 6/13	FAB C	REV A
---------------------------	-------------------------------	--------------	----------	----------

8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

### SMC AND SPI INTERFACES

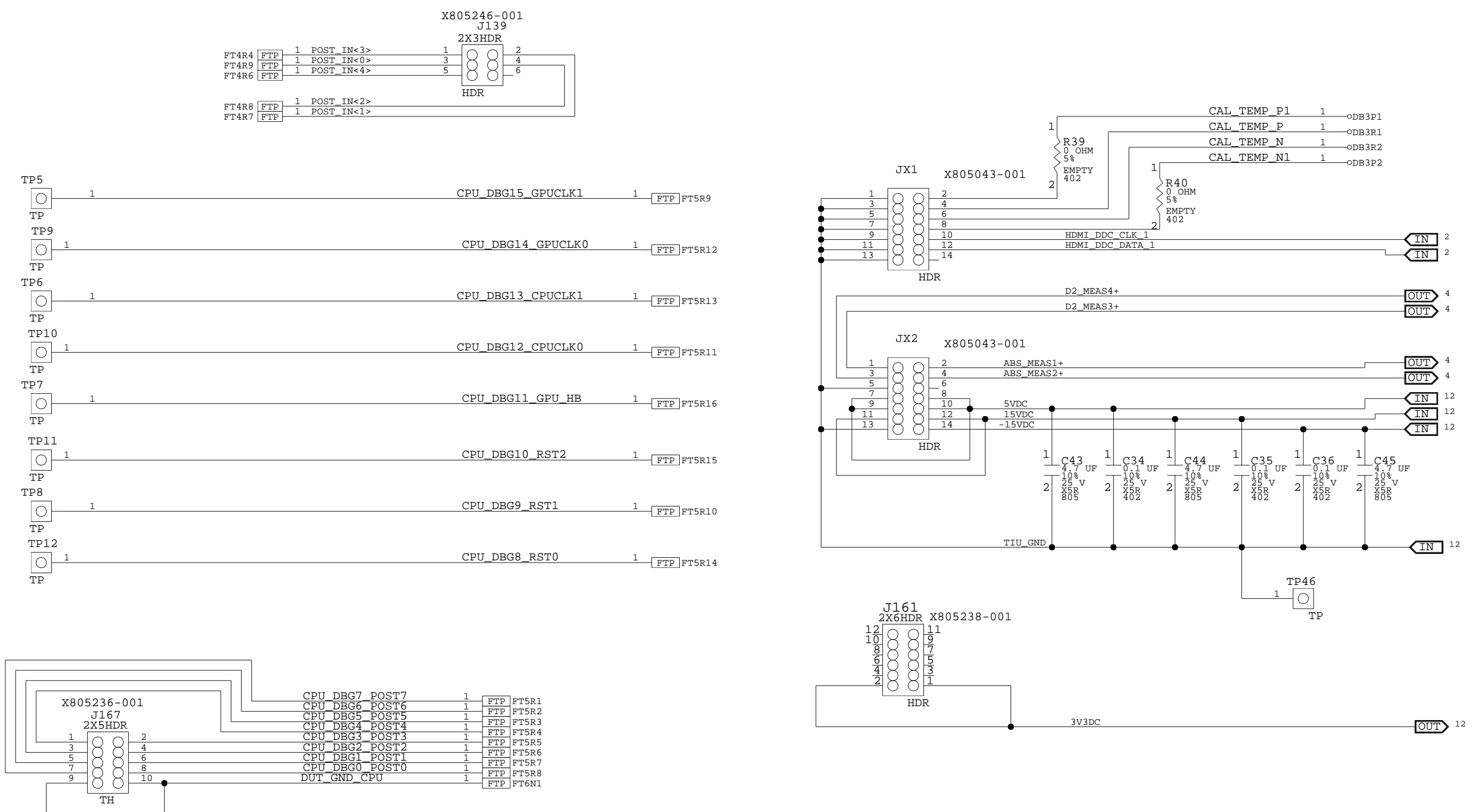


NET	CORONA PCBA	CORONA BSCAN	WINCHESTER PCBA	WINCHESTER BSCAN
CPU_TRST_N	OPEN	OPEN / BY POD	OPEN	PULLED LOW
GPU_TRST_N	OPEN	OPEN / BY POD	OPEN	PULLED LOW
GPU_TRST_ED_N	OPEN	OPEN / BY POD	N/A	N/A
SB_TRST	OPEN	DRIVEN BY POD	OPEN	DRIVEN BY POD
CPU_RST_N	OPEN	OPEN	OPEN	PULLED LOW
GPU_RST_N	OPEN	OPEN	OPEN	PULLED LOW
BSC_CHIP_DI_B	N/A	N/A	OPEN	PULLED LOW
PEX_TRST_N	N/A	N/A	OPEN	PULLED LOW
BS_TRST_N	N/A	N/A	OPEN	DRIVEN BY POD

MS DOC#: H103204

8 7 6 5 4 3 2 1

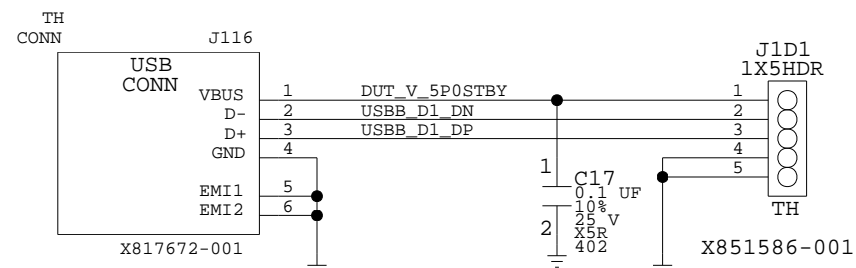
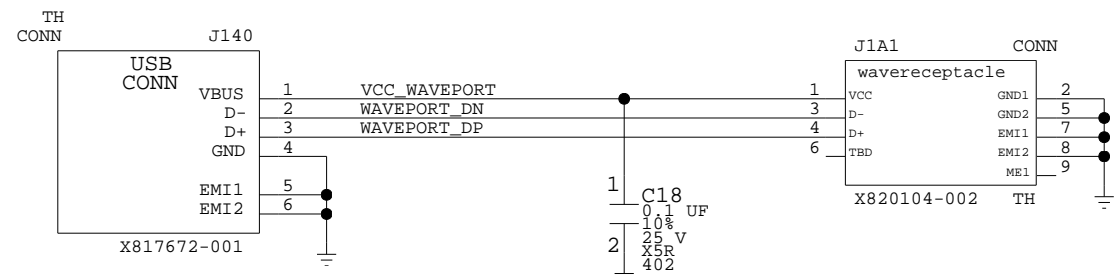
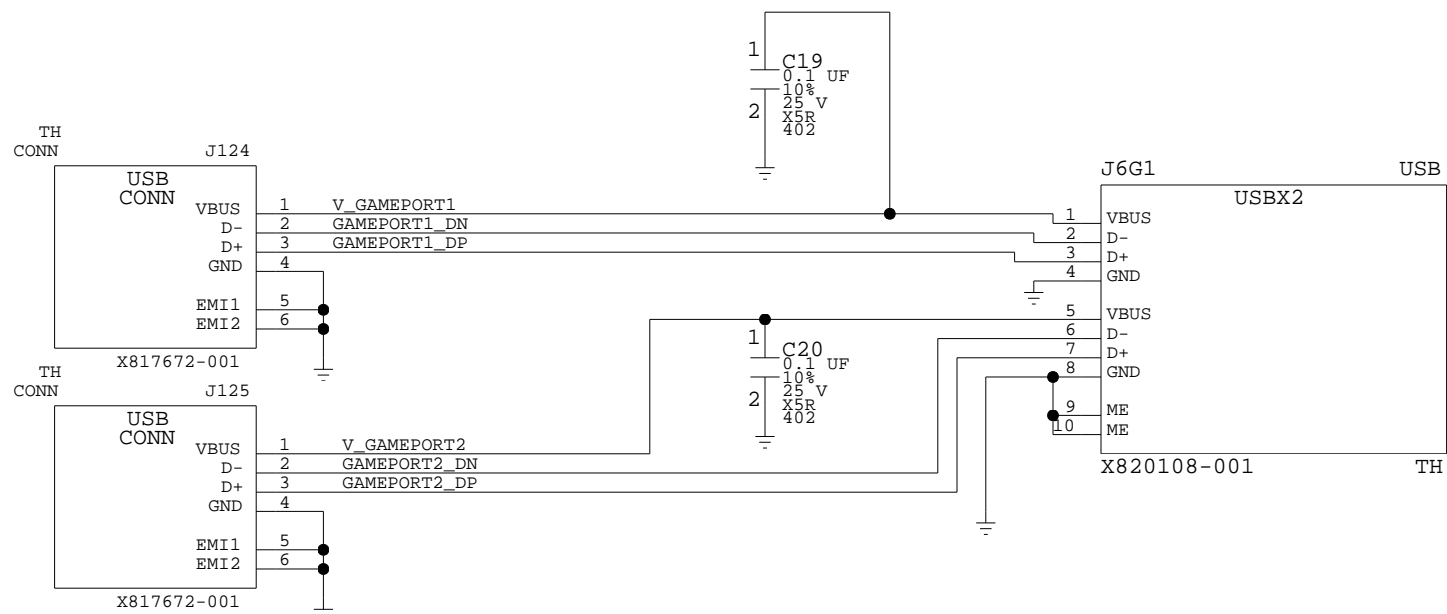
# DEBUG AND THERMAL CAL INTERFACES



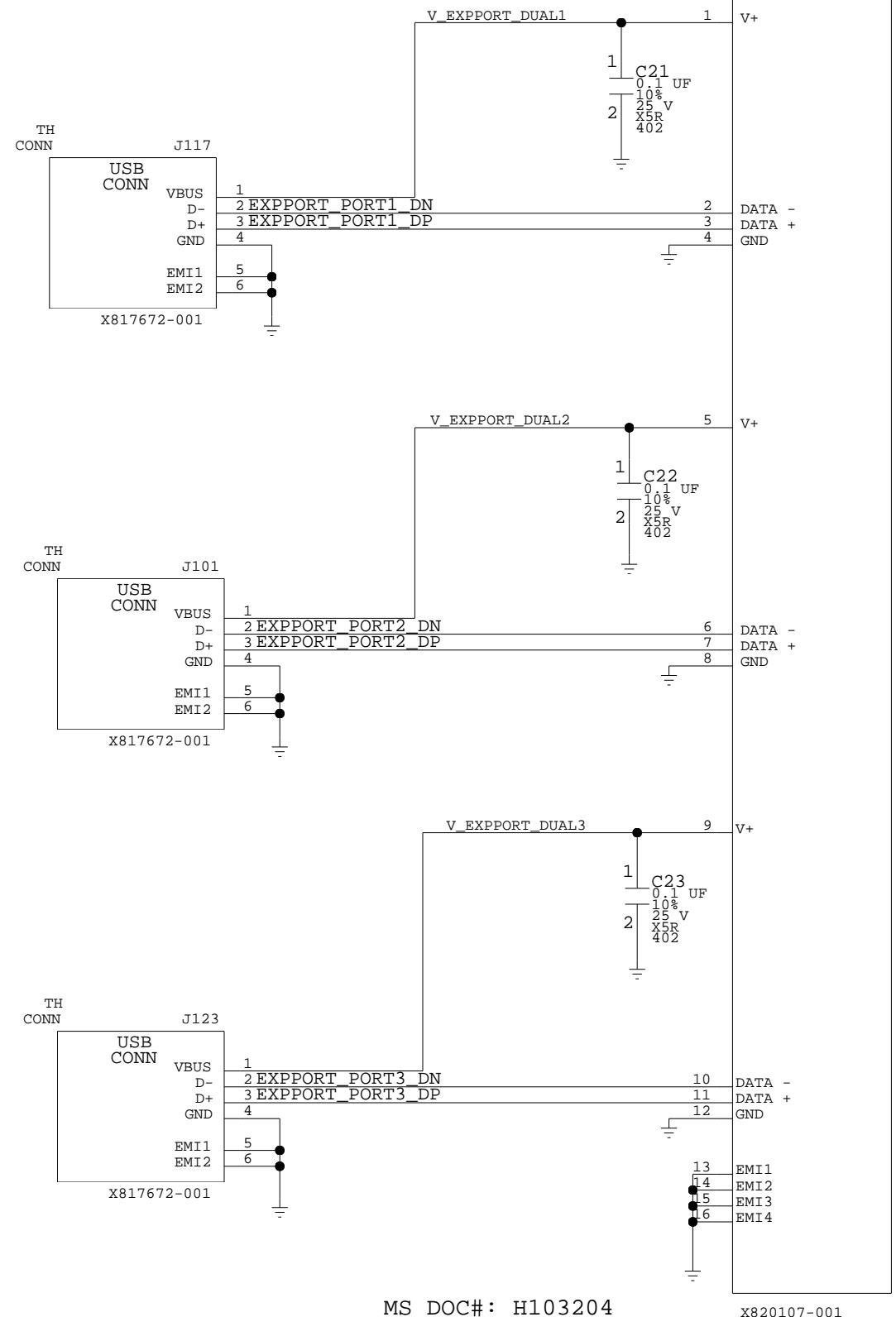
MS DOC#: H103204



# USB AND WAVE INTERFACES



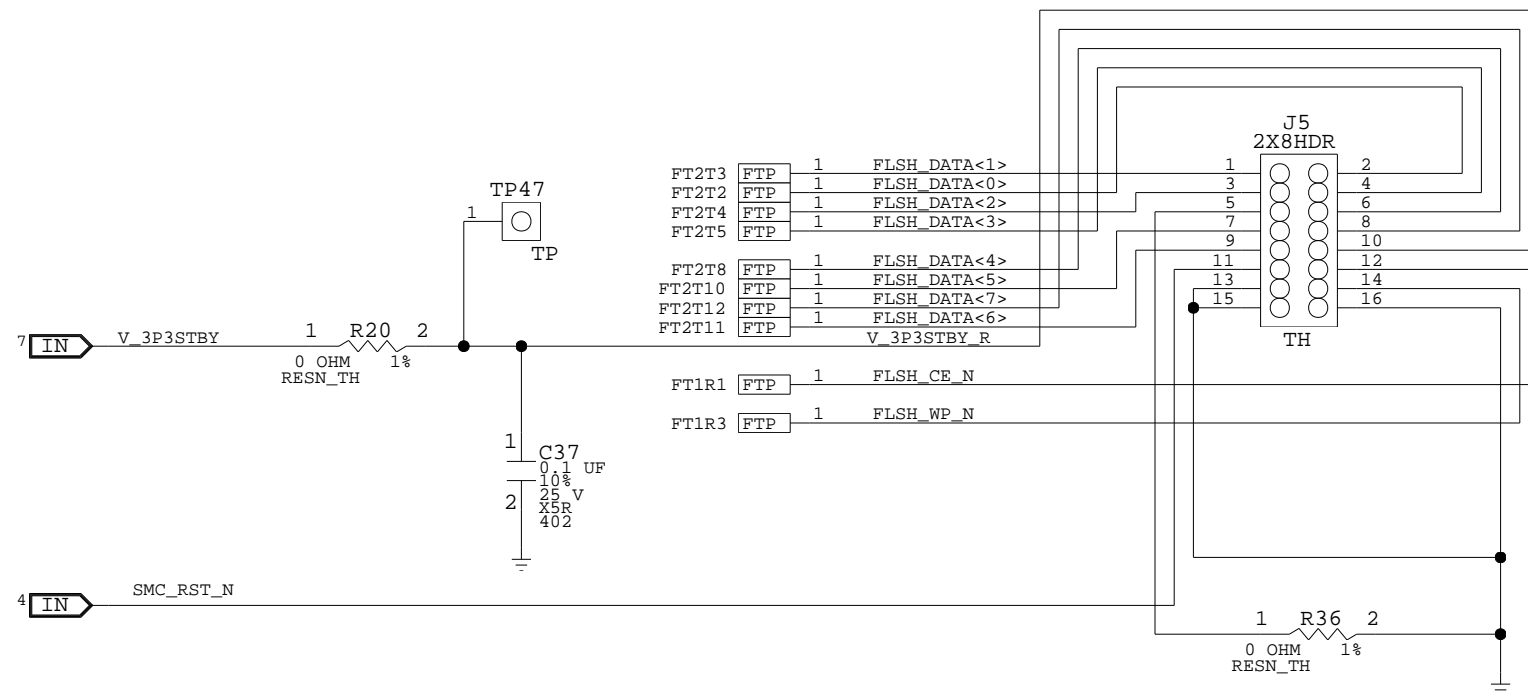
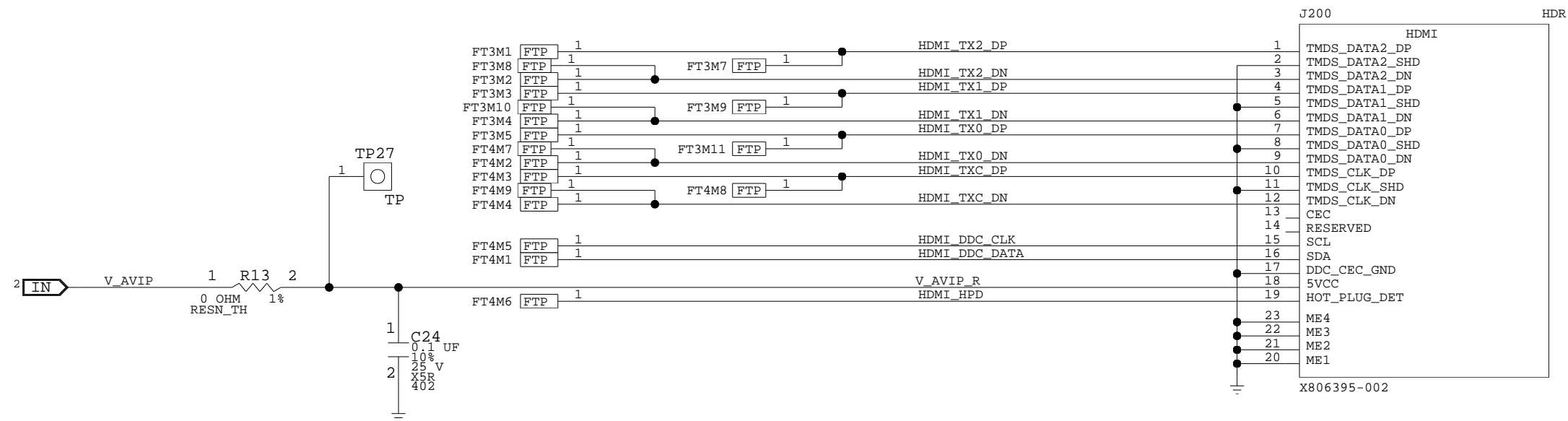
# VERTICAL TRIPLE USB



MS DOC#: H103204

X820107-001

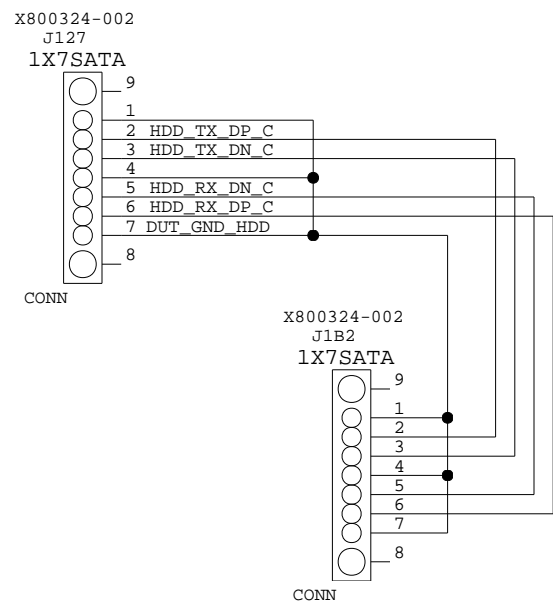
# HDMI AND MMC INTERFACES



MS DOC#: H103204

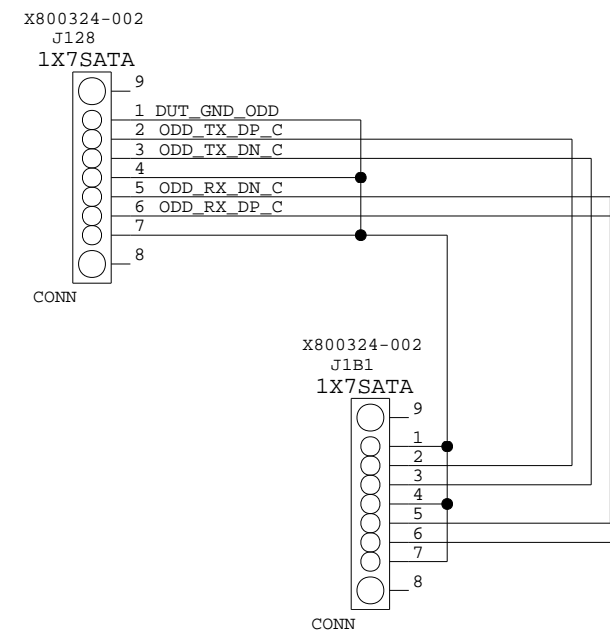
# HDD AND ODD INTERFACES

## HDD SATA



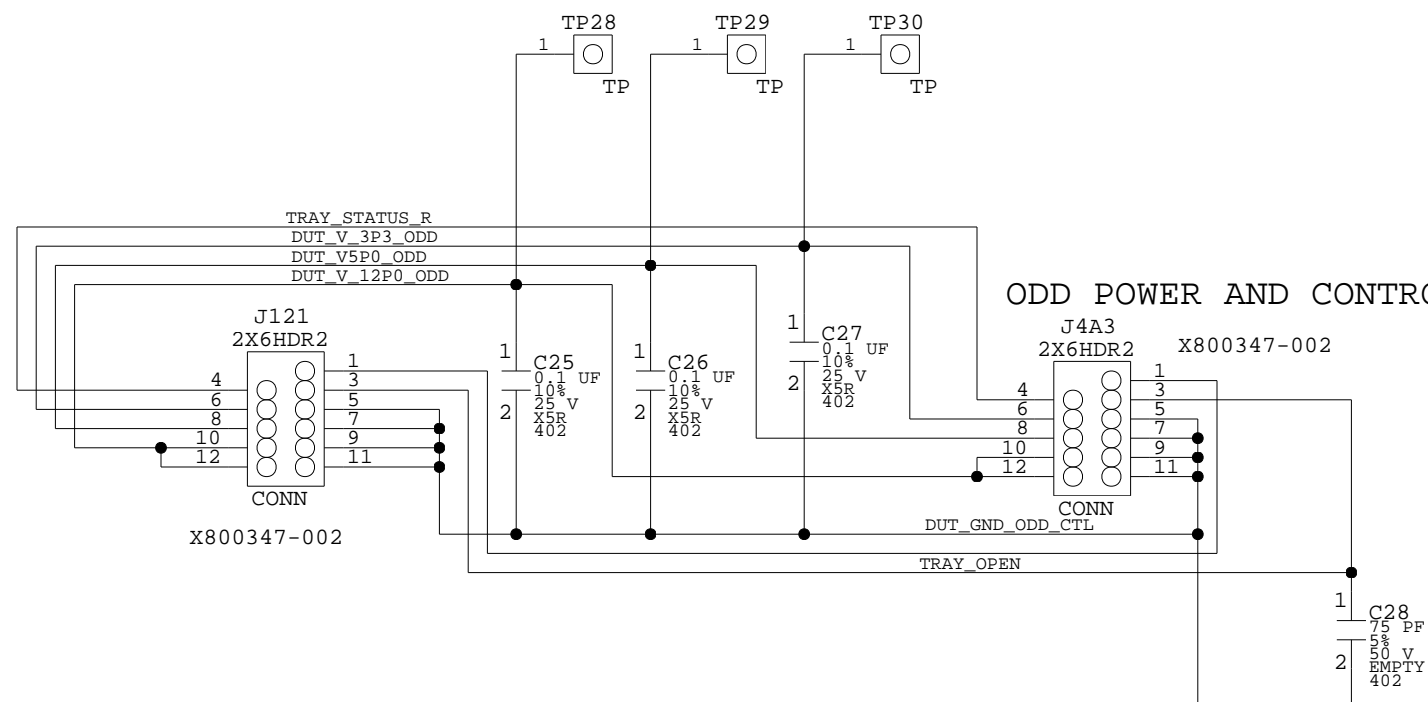
FT1N4 - FT1N8 ARE NOT USED

## ODD SATA

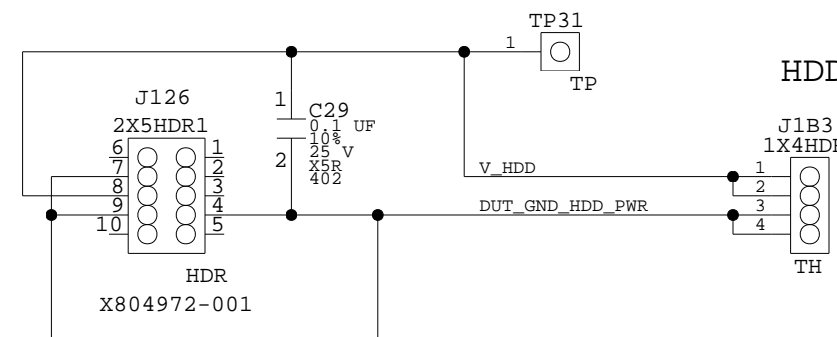


FT1N2 - FT1N3 AND FT1M3 - FT1M5 ARE NOT USED

## ODD POWER AND CONTROL

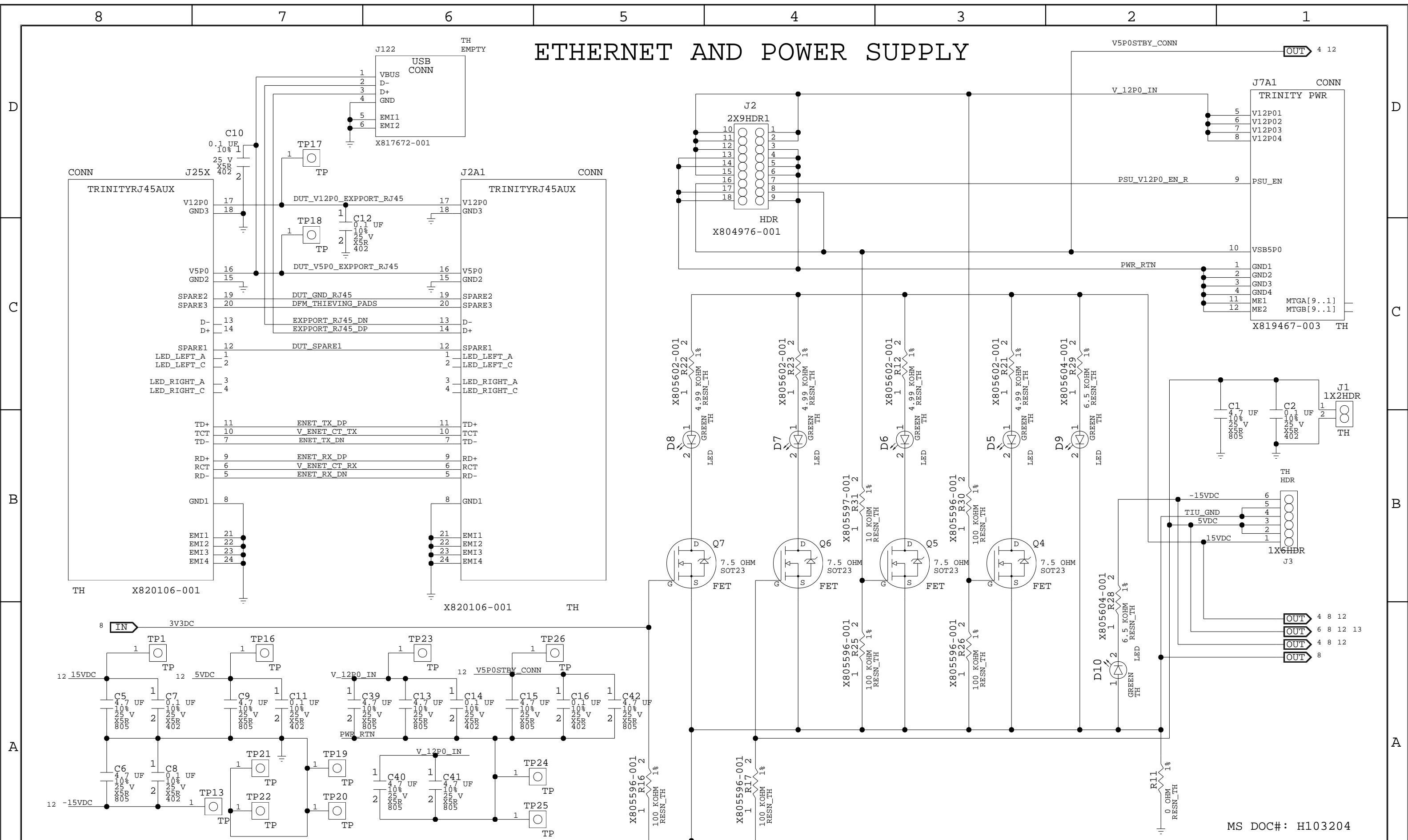


## HDD POWER



MS DOC#: H103204

# ETHERNET AND POWER SUPPLY

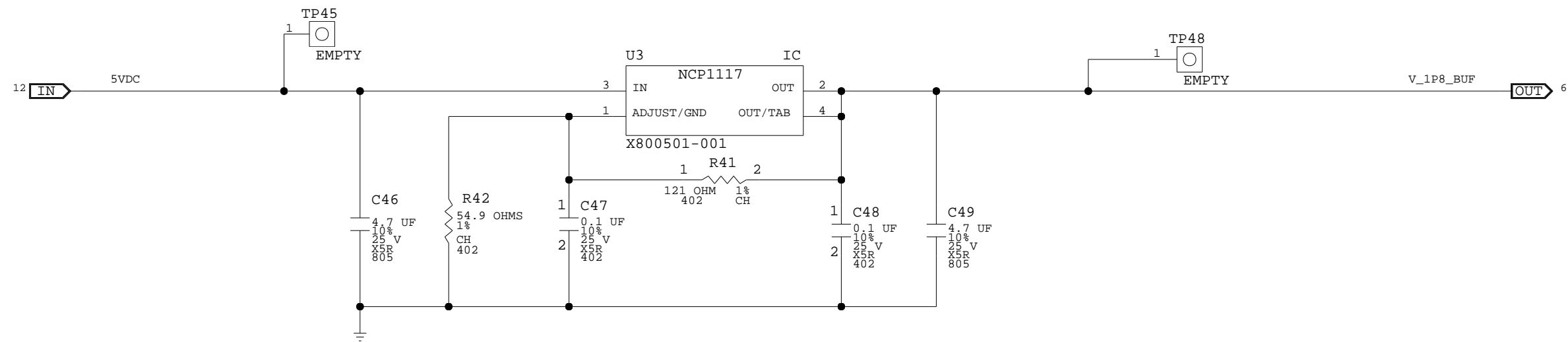


[PAGE\_TITLE=ETHERNET AND POWER SUPPLY]

DRAWING  
Thu Jan 20 04:54:35 2011

MICROSOFT CONFIDENTIAL	PROJECT NAME Humming Xlate	PAGE 12/13	FAB C	REV A
---------------------------	-------------------------------	---------------	----------	----------

MS DOC#: H103204



[PAGE\_TITLE=BUFFER POWER SUPPLY]

MS DOC#: H103204

DRAWING  
Thu Jan 20 04:54:35 2011

MICROSOFT CONFIDENTIAL	PROJECT NAME Humming Xlate	PAGE 13/13	FAB C	REV A
---------------------------	-------------------------------	---------------	----------	----------