



Display

Date: **December 4, 2000**

Model: **BKM-21D**

Subject: **VERTICAL BLUE STRIPES ON RIGHT SIDE OF SCREEN**

Serial No: **2,000,001 AND HIGHER**

**DESCRIPTION**

When feeding a PAL analog video output signal (DVW-A500P built-in pathological signal in 625/50 narrow blanking mode) to an analog input and a picture is received, vertical blue stripes may appear on the right side of magenta in the upper half of the pathological signal.

To correct the symptom, perform the following modification procedure to add a delay circuit to the blanking pulse circuit so the output pulse does not overlap the video signal.

**PARTS REQUIRED**

Part No.	Description	Qty.
1-101-361-00	Cap, Ceramic, 150pF	1
1-215-439-00 <sup>a</sup> or 1-215-447-00	Res, Metal, 5.6kΩ or Res, Metal, 12kΩ	1

a. See Step 2f to determine which resistor is required for this procedure.

**NOTE:** This modification procedure also requires the following parts:

- 20mm jumper
- Two 170mm jumpers
- RTV

**MODIFICATION PROCEDURE**

**BD Board**

1. Verify blanking pulse phase as follows:

a. Install BKM-21D.

**NOTE:** Remove BKM-25P board from monitor, if installed.

b. Feed DVW-A500P's built-in pathological signal to INPUT-4 of BKM-21D.

c. Set INPUT CONFIG as follows:

FORMAT: PAL-S

SLOT NO.: X (X=slot in which BKDM-21D is installed.)

INPUT NO.: 4

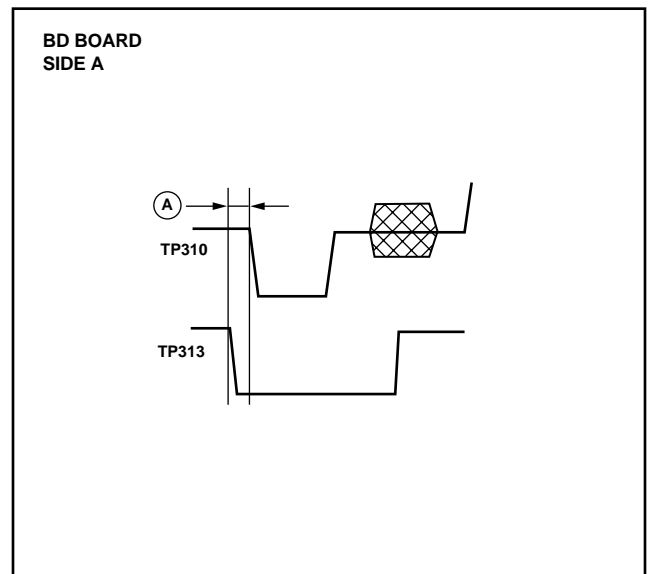
d. Receive pathological signal.

e. Connect oscilloscope probes to TP310 (PAL signal) and TP313 (H BLK) on BD board.

f. Measure time difference between leading edges of sync signal at TP310 and signal at TP313. (See "A" in Figure 1.)

Use this measurement to determine which resistor is required in step 2f.

**NOTE:** If the leading edge of the sync signal at TP313 lags the leading edge of the sync signal at TP310, step 2 of this modification procedure is not necessary.



**Figure 1**



2. Add delay circuit to BD board (side B) as follows:
  - a. Cut trace between IC311 pin 5 and land A. (See Figure 2.)
  - b. Solder 150pF capacitor between IC311 pins 5 and 8 (GND).
  - c. Cut traces between: (See Figure 2.)
    - IC140 pins 14 and 13
    - IC140 pins 13 and 12
  - d. Solder 20mm jumper between IC140 pin 13 and C175 (GND side). (See Figure 2.)
  - e. Solder 170mm jumper between IC140 pin 12 and land A.
  - f. Solder one of the following resistors to IC140 pin 11. The resistor to use depends on the blanking pulse phase determined in step 1f as follows:

If the leading edge of the sync signal at TP313 leads the leading edge of the sync signal at TP310 by:

- 0.5µs or less, use metal resistor 5.6kΩ (1-215-439-00).
- more than 0.5µs, use metal resistor 12kΩ (1-215-447-00).

- g. Solder 170mm jumper between resistor soldered in step 2f, and IC311 pin 5.
- h. Affix jumpers, capacitor, and resistor to board with RTV.
- i. Perform “Analog PAL Mode Adjustment” and “PAL H BLK Pulse Adjustment” as described on page 4-11 of the operation and maintenance manual.

**NOTE:** Use Specification A=2.8±0.1µs.

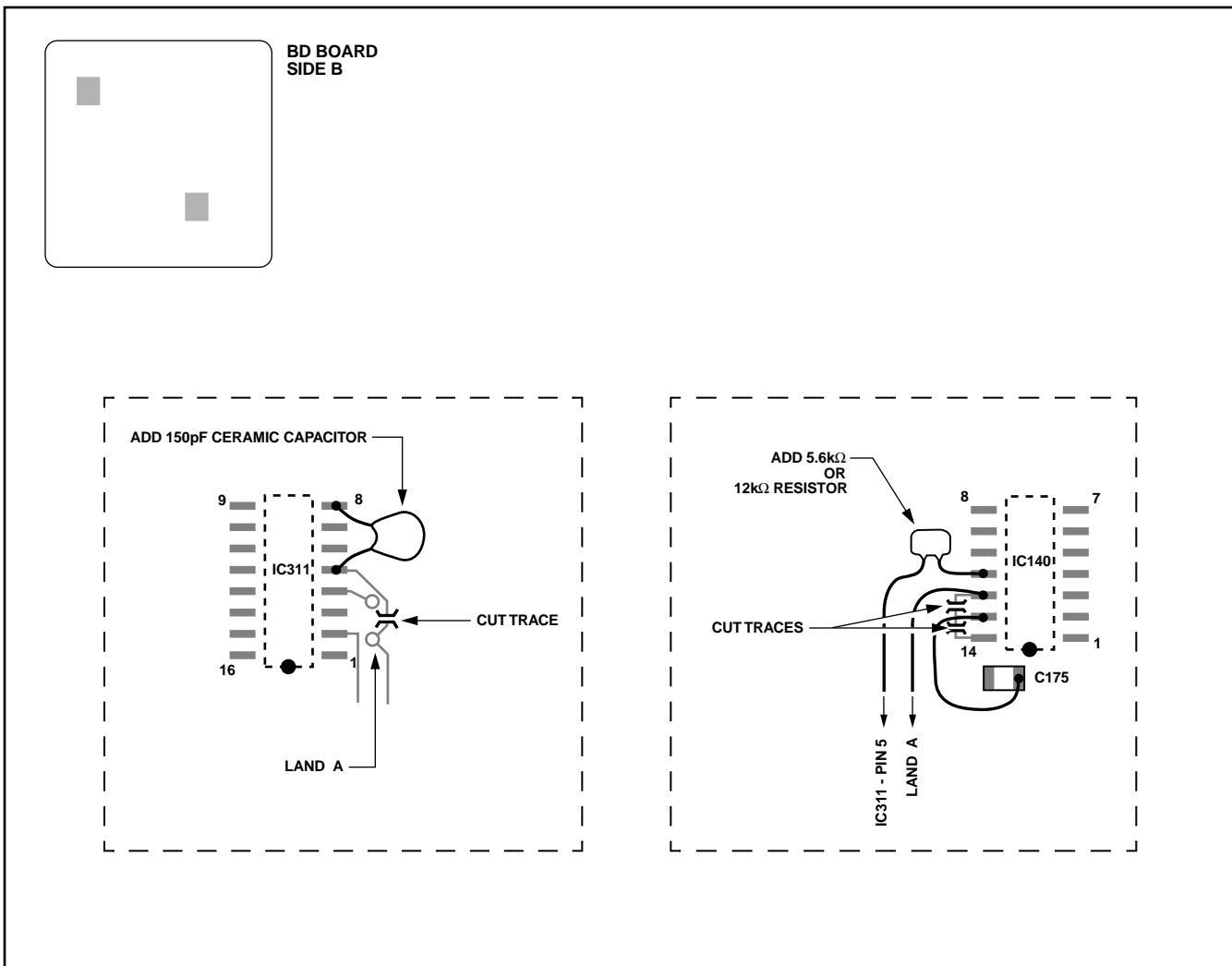


Figure 2